

<b>Notice of References Cited</b>	Application/Control No. 10/026,078	Applicant(s)/Patent Under Reexamination AUBURY, MATTHEW PHILIP	
	Examiner Luke Osborne	Art Unit 2123	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,678,028	10-1997	Bershteyn et al.	703/22
	B	US-5,870,588	02-1999	Rompaey et al.	703/13
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	J.M. Saul, "Hardware / Software Codesign for FPGA-based Systems" HICSS-32, 5-8 Jan. 1999 Page(s):10 pp.
	V	M. F. Parkinson and S. Parameswaran, "Profiling in the ASP Codesign Enviroment", System Synthesis, 1995., Proceedings of the Eighth International Symposium on 13-15 Sept. 1995, Page(s):128 - 133
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.